

## **ABSTRACT OF THE DISCLOSURE**

The present invention provides a data processing apparatus and slave interface

- 5 mechanism for controlling access to a slave logic unit by a plurality of master logic units. The data processing apparatus comprises a first bus for coupling a first master logic unit with a plurality of slave logic units to enable the first master logic unit to issue a first transfer request to any of said slave logic units, and a second bus for coupling a second master logic unit with a subset of said plurality of slave logic units to enable the second master logic unit to issue a second transfer request to any of the slave logic units in the subset. A slave interface mechanism is then associated with each slave logic unit in the subset and comprises switching logic arranged to connect either the first bus or the second bus to the corresponding slave logic unit to enable either the first transfer request or the second transfer request to be routed to that slave logic unit. By this approach,  
10 unrelated transfer requests can be processed in parallel.

卷之三